

Application No.: 10/604,627  
Docket NO.: 11112-US-PA

## REMARKS

### Present Status of the Application

This is a full and timely response to the outstanding final Office Action mailed on September 23, 2004. It is noted with great appreciation that the Examiner considers claims 10-17 as allowed and claim 3 being allowable if rewritten in independent form including all of the limitations of the base claim and any intervening claims. The Office Action, however, rejected claims 1-2, 4-5 and 7-9 under 35 U.S.C. § 102(b) as being anticipated by Admitted Prior Art Fig. 1. The Office Action further objected to the title of the invention.

Claims 1-5 and 7-17 remain pending of which claims 10-17 have been amended to have the correct claim numbers. It is believed that no new matter is added by way of these amendments made to the claims or otherwise to the application.

After carefully considering the remarks set forth in this Office Action and the cited references, Applicants respectfully submitted that the presently pending claims are in condition for allowance. Reconsideration and withdrawal of the Examiner's rejection are requested.

### Discussion of the Office Action Objections

*The title of the invention is objected to for not being descriptive.*

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In response thereto, the title of the invention has been amended to read "Resistance Random Access Memory" to clearly be indicative of the invention to which the claims are directed.

**Discussion of Office Action Rejections**

*The Office Action rejected claims 1-2, 4-5, and 7-9 under 35 U.S.C. 102(a,b) as being anticipated by Admitted Prior Art Figure 1.*

In order to properly anticipate Applicant's claimed invention under 35 U.S.C. § 102, each and every element of the claim in issue must be found, "either expressly or inherently described, in a single prior art reference." "The identical invention must be shown in as complete detail as contained in the ... claim. *Richardson v. Suzuki Motor Co.*, 868 F.2d 1226, 1236, 9 USPQ2d 1913, 1920 (Fed. Cir. 1989).

Claim 1 provides a resistance random access memory structure comprising a plurality of word lines, a plurality of reset lines, a dielectric layer, a plurality of memory units and a plurality of bit lines, and further recites a limitation that "...bottom electrodes of said memory units in a same column being coupled to the same one of said reset lines ...". The Office Action, however, asserts that the AAP also teaches the bottom electrodes of the memory units in a same column being coupled to one of the reset line 104. Applicants respectfully disagree. As shown in Figure 1, the bottom electrodes of the two memory units in a same column are coupled to two different reset lines, instead of the same reset line as taught in claim 1. As

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clearly demonstrated in Figure 6A, the bottom electrodes 308a of the memory units in a same column are coupled to a same rest line 304.

For at least these reasons, Applicants respectfully assert that claim 1 patentably define over the Admitted Prior Art. Since claims 2, 4-5, and 7-9 are dependent claims which further define the invention recited in claim 1, Applicant respectfully assert that these claim is also in condition for allowance. Therefore, reconsideration and withdrawal of these rejections are respectfully requested.

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### CONCLUSION

For at least the foregoing reasons, it is believed that the presently pending claims 1-5, 7-9 and 18-25 are in proper condition for allowance. If the Examiner believes that a telephone conference would expedite the examination of the above-identified patent application, the Examiner is invited to call the undersigned.

Date:

*Dec. 21, 2004*

Respectfully submitted,

*Belinda Lee*

Belinda Lee

Registration No.: 46,863

Jianq Chyun Intellectual Property Office  
7<sup>th</sup> Floor-1, No. 100  
Roosevelt Road, Section 2  
Taipei, 100  
Taiwan  
Tel: 011-886-2-2369-2800  
Fax: 011-886-2-2369-7233  
Email: [belinda@jciigroup.com.tw](mailto:belinda@jciigroup.com.tw)  
[Usa@jciigroup.com.tw](mailto:Usa@jciigroup.com.tw)